

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 02/20/04
Application Type:: Regular
Subject Matter:: Utility
Title:: Tuning Programmable Logic Devices
for Low-Power Design Implementation
Attorney Docket Number:: X-1462-1P US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 9
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Tim
Family Name:: Tuan
Street:: 3286 Payne Avenue
City:: San Jose
State or Province:: CA
Postal or Zip Code:: 95117

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Jan
Middle Name:: L.
Family Name:: de Jong
Street:: 19871 Rodrigues Avenue
City:: Cupertino
State or Province:: CA

Postal or Zip Code:: 95014

Applicant Authority Type:: Inventor

Primary Citizenship Ctry:: US

Status:: Full Capacity

Given Name:: Kameswara

Middle Name:: K.

Family Name:: Rao

Street:: 1402 Graywood Drive

City:: San Jose

State or Province:: CA

Postal or Zip Code:: 95129

Applicant Authority Type:: Inventor

Primary Citizenship Ctry:: US

Status:: Full Capacity

Given Name:: Robert

Middle Name:: O.

Family Name:: Conn

Street:: 15210 Old Ranch Road

City:: Los Gatos

State or Province:: CA

Postal or Zip Code:: 95033

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Continuation In Part	10/666,669	9/19/2003

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124